

DEC 30 2003

PATENT

Attorney Docket No. 401407/FUKAMI

In re Application of: KANAYA et al.
 Application No. 09/972,955
 Filed: October 10, 2001
 For: High-Frequency Semiconductor Integrated Circuit Capable of Switching between Characteristics Thereof

Mail Stop AF
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Corres. and Mail
 BOX AF

Sir:

Small entity status is claimed for this application under 37 CFR 1.27.

Petition for an extension of time for the period noted below, as well as for any additional period necessary to render the present submission timely. Please charge Deposit Account No. 12-1216 for the appropriate petition fee.

Other:

Please charge Deposit Account No. 12-1216 in the total amount indicated below. A duplicate copy of this transmittal sheet is enclosed herewith.

						SMALL ENTITY	OTHER THAN A SMALL ENTITY	
TIME EXTENSION PETITION FEE		none		\$ 0.00		\$ 0.00	\$ 0.00	
		subtract time extension fee previously paid		none		(\$ 0.00)	(\$ 0.00)	
CLAIM FEE	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	EXTRA CLAIMS PRESENT	RATE	ADDT. CLAIM FEE	RATE	ADDT. CLAIM FEE
TOTAL	14	MINUS	15	=00	x 9=	\$	x 18=	\$00
INDEPENDENT	3	MINUS	3	=00	x 43=	\$	x 86=	\$00
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE CLAIM					+ 145=	\$	+ 290=	\$
TOTAL AMOUNT TO BE CHARGED TO DEPOSIT ACCOUNT					TOTAL	\$	TOTAL	\$00

The Commissioner is hereby authorized to charge any deficiencies in the following fees associated with this communication or credit any overpayment to Deposit Account No. 12-1216.

Any filing fees under 37 CFR 1.16 for the presentation of extra claims.

Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

Jeffrey A. Wyand, Reg. No. 29,458
 LEYDIG, VOIT & MAYER
 700 Thirteenth Street, N.W., Suite 300
 Washington, DC 20005-3960
 (202) 737-6770 (telephone)
 (202) 737-6776 (facsimile)

Date: December 30, 2003
 JAW/nc



**RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2817**

PATENT
Attorney Docket No. 401407

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

KANAYA et al.

Application No. 09/972,955

Art Unit: 2817

Examiner: S. Jones

Filed: October 10, 2001

For: **HIGH-FREQUENCY
SEMICONDUCTOR INTEGRATED
CIRCUIT CAPABLE OF SWITCHING
BETWEEN CHARACTERISTICS
THEREOF**

RESPONSE TO OFFICE ACTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated October 29, 2003, please enter the following amendments and consider the following remarks.